



Product/Process Change Notice - PCN 24_0009 Rev. C

Analog Devices, Inc. One Analog Way, Wilmington, MA 01887, USA

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. An acceptance or concern response should be submitted to ADI promptly. Any requests for samples of changed material or additional information must be made within 30 days of the notification. In accordance with JEDEC Standard 046, customers should acknowledge receipt of the PCN within 30 days of the PCN delivery. ADI contact information is listed below. Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change. After the acknowledgment, a lack of additional requests within 90 days constitutes acceptance of the change.

Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

PCN Title:	Qualification of Alternative Wafer Fab for TSMC 0.18um Mixed Signal CMOS Process
Publication Date:	16-Dec-2024
Effectivity Date:	20-Mar-2025 <i>(the earliest date that a customer could expect to receive changed material)</i>
Revision Description:	Remove parts. Add updated Qualification Results.

Description Of Change:

Analog Device is adding Analog Devices Beaverton OR, USA (ADBN) as an alternate Wafer Fab site to TSMC Taiwan for 0.18um Mixed Signal CMOS Process.

Reason For Change:

This change will ensure manufacturing agility and continuity of supply.

Impact of the change (positive or negative) on fit, form, function & reliability:

There is no impact to form, fit, function or reliability.

Product Identification: *(this section will describe how to identify the changed material)*

ADI maintains traceability internally to manufacturing location site via marking on the device.

Summary of Supporting Information:

Qualification has been performed per Industry Standard Test Methods. See attached Qualification Results.

Supporting Documents:

Attachment 1: Type: Delta Qualification Matrix

ADI_PCN_24_0009_Rev_C_PCN-Delta-Qualification-Matrix-ZVEI-5_0_16 Correction May2024.xlsm

Attachment 2: Type: Qualification Results Summary

ADI_PCN_24_0009_Rev_C_Qualification Results.pdf

Note: If applicable, the device material declaration will be updated due to material change.

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Korea:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_Korea@analog.com	PCN_ROA@analog.com

Appendix A - Affected ADI Models:

Existing Parts - Product Family / Model Number (168)

AD8283 / AD8283WBCPZ	AD8283 / AD8283WBCPZ-RL	AD8285 / AD8285WBCPZ	AD8285 / AD8285WBCPZ-RL	AD9212 / AD9212ABCPZ-40
AD9212 / AD9212ABCPZ-65	AD9222 / AD80418BCPZ-65	AD9222 / AD9222ABCPZ-40	AD9222 / AD9222ABCPZ-50	AD9222 / AD9222ABCPZ-65
AD9222 / AD9222ABCPZRL7-50	AD9252 / AD9252ABCPZ-50	AD9252 / AD9252ABCPZRL7-50	AD9253 / AD9253BCPZ-105	AD9253 / AD9253BCPZ-125
AD9253 / AD9253BCPZ-80	AD9253 / AD9253BCPZRL7-105	AD9253 / AD9253BCPZRL7-125	AD9253 / AD9253BCPZRL7-80	AD9255 / AD9255BCPZ-105
AD9255 / AD9255BCPZ-125	AD9255 / AD9255BCPZ-80	AD9255 / AD9255BCPZRL7-105	AD9255 / AD9255BCPZRL7-125	AD9255 / AD9255BCPZRL7-80
AD9257 / AD9257BCPZ-40	AD9257 / AD9257BCPZ-65	AD9257 / AD9257BCPZRL7-40	AD9257 / AD9257BCPZRL7-65	AD9265 / AD89025BCPZ
AD9265 / AD89025BCPZRL7	AD9265 / AD9265BCPZ-105	AD9265 / AD9265BCPZ-125	AD9265 / AD9265BCPZ-80	AD9265 / AD9265BCPZRL7-105
AD9265 / AD9265BCPZRL7-125	AD9265 / AD9265BCPZRL7-80	AD9608 / AD9608BCPZ-105	AD9608 / AD9608BCPZ-125	AD9608 / AD9608BCPZRL7-105
AD9608 / AD9608BCPZRL7-125	AD9609 / AD9609BCPZ-20	AD9609 / AD9609BCPZ-40	AD9609 / AD9609BCPZ-65	AD9609 / AD9609BCPZ-80
AD9609 / AD9609BCPZRL7-20	AD9609 / AD9609BCPZRL7-40	AD9609 / AD9609BCPZRL7-65	AD9609 / AD9609BCPZRL7-80	AD9628 / AD9628BCPZ-105
AD9628 / AD9628BCPZ-125	AD9628 / AD9628BCPZRL7-105	AD9628 / AD9628BCPZRL7-125	AD9629 / AD9629BCPZ-20	AD9629 / AD9629BCPZ-40
AD9629 / AD9629BCPZ-65	AD9629 / AD9629BCPZ-80	AD9629 / AD9629BCPZRL7-20	AD9629 / AD9629BCPZRL7-40	AD9629 / AD9629BCPZRL7-65
AD9629 / AD9629BCPZRL7-80	AD9633 / AD9633BCPZ-105	AD9633 / AD9633BCPZ-125	AD9633 / AD9633BCPZ-80	AD9633 / AD9633BCPZRL7-105
AD9633 / AD9633BCPZRL7-125	AD9633 / AD9633BCPZRL7-80	AD9635 / AD9635BCPZ-125	AD9635 / AD9635BCPZ-80	AD9635 / AD9635BCPZRL7-125
AD9635 / AD9635BCPZRL7-80	AD9637 / AD9637BCPZ-40	AD9637 / AD9637BCPZ-80	AD9637 / AD9637BCPZRL7-40	AD9637 / AD9637BCPZRL7-80
AD9645 / AD9645BCPZ-125	AD9645 / AD9645BCPZ-80	AD9645 / AD9645BCPZRL7-125	AD9645 / AD9645BCPZRL7-80	AD9648 / AD9648BCPZ-105
AD9648 / AD9648BCPZ-125	AD9648 / AD9648BCPZRL7-105	AD9648 / AD9648BCPZRL7-125	AD9649 / AD9649BCPZ-20	AD9649 / AD9649BCPZ-40
AD9649 / AD9649BCPZ-65	AD9649 / AD9649BCPZ-80	AD9649 / AD9649BCPZRL7-20	AD9649 / AD9649BCPZRL7-40	AD9649 / AD9649BCPZRL7-65
AD9649 / AD9649BCPZRL7-80	AD9704 / AD9704BCPZ	AD9704 / AD9704BCPZRL7	AD9705 / AD9705BCPZ	AD9705 / AD9705BCPZRL7
AD9706 / AD9706BCPZ	AD9706 / AD9706BCPZRL7	AD9707 / AD9707BCPZ	AD9707 / AD9707BCPZRL7	AD9714 / AD9714BCPZ
AD9714 / AD9714BCPZRL7	AD9715 / AD9715BCPZ	AD9715 / AD9715BCPZRL7	AD9716 / AD9716BCPZ	AD9716 / AD9716BCPZRL7
AD9717 / AD9717BCPZ	AD9717 / AD9717BCPZRL7	AD9911 / AD9911BCPZ	AD9911 / AD9911BCPZ-REEL7	AD9958 / AD9958BCPZ
AD9958 / AD9958BCPZ-REEL7	AD9959 / AD9959BCPZ	AD9959 / AD9959BCPZ-REEL7	ADA4355 / ADA4355ABCZ	ADAU1361 / ADAU1361BCPZ
ADAU1361 / ADAU1361BCPZ-R7	ADAU1361 / ADAU1361BCPZ-RL	ADAU1401 / AD71073YSTZ-RL	ADAU1401 / ADAU1401YSTZ	ADAU1401 / ADAU1401YSTZ-RL
ADAU1701 / ADAU1701JSTZ	ADAU1701 / ADAU1701JSTZ-RL	ADAU1702 / ADAU1702JSTZ	ADAU1702 / ADAU1702JSTZ-RL	ADAU1761 / ADAU1761BCPZ
ADAU1761 / ADAU1761BCPZ-R7	ADAU1761 / ADAU1761BCPZ-RL	ADCLK846 / ADCLK846BCPZ	ADCLK846 / ADCLK846BCPZ-REEL7	ADCLK854 / ADCLK854BCPZ
ADCLK854 / ADCLK854BCPZ-REEL7	ADDR9501 / ADDR9501BCPZ	ADDR9501 / ADDR9501BCPZRL	ADDR9501 / ADW15001YCFZ	ADDR9501 / ADW15001YCFZRL
ADE9000 / ADE9000ACPZ	ADE9000 / ADE9000ACPZ-RL	ADE9000A / ADE9000AACPZ	ADE9000A / ADE9000AACPZ-RL	ADE9039 / ADE9039ACPZ
ADE9039 / ADE9039ACPZ-RL	ADE9078 / ADE9078ACPZ	ADE9078 / ADE9078ACPZ-RL	ADE9153A / ADE9153AACPZ	ADE9153A / ADE9153AACPZ-RL
ADE9153B / ADE9153BACPZ	ADE9153B / ADE9153BACPZ-RL	ADE9430 / ADE9430ACPZ	ADE9430 / ADE9430ACPZ-RL	ADF7023 / AD57/005Z-0RL
ADF7023 / ADF7023BCPZ	ADF7023 / ADF7023BCPZ-RL	ADF7023-J / ADF7023-JBCPZ	ADF7023-J / ADF7023-JBCPZ-RL	ADF7024 / ADF7024BCPZ
ADF7024 / ADF7024BCPZ-RL	ADV7180 / ADV7180WBCPZ	ADV7180 / ADV7180WBCPZ-REEL	ADV7343 / ADV7343WBSTZ	ADV7343 / ADV7343WBSTZ-RL
ADV7390 / ADV7390WBCPZ	ADV7390 / ADV7390WBCPZ-RL	ADV7391 / ADV7391WBCPZ	ADV7391 / ADV7391WBCPZ-RL	ADV7392 / ADV7392WBCPZ
ADV7392 / ADV7392WBCPZ-REEL	ADV7393 / ADV7393WBCPZ	ADV7393 / ADV7393WBCPZ-REEL		

Appendix A - Affected ADI Models:::

Removed Parts On All Revisions - Product Family / Model Number (30)

ADA8282 / ADA8282WBCPZ	ADA8282 / ADA8282WBCPZ-R7	ADAR7251 / ADAR7251WBSCZ	ADAR7251 / ADAR7251WBSCZ-RL	ADV7180 / ADV7180BCP32Z
ADV7180 / ADV7180BCP32Z-RL	ADV7180 / ADV7180BCPZ	ADV7180 / ADV7180BCPZ-REEL	ADV7180 / ADV7180BST48Z	ADV7180 / ADV7180BST48Z-RL
ADV7180 / ADV7180BSTZ	ADV7180 / ADV7180BSTZ-REEL	ADV7180 / ADV7180KCP32Z	ADV7180 / ADV7180KCP32Z-RL	ADV7180 / ADV7180KST48Z
ADV7180 / ADV7180KST48Z-RL	ADV7180 / ADV7180WBCP32Z	ADV7180 / ADV7180WBCP32Z-RL	ADV7180 / ADV7180WBST48Z	ADV7180 / ADV7180WBST48Z-RL
ADV7180 / ADV7180WBSTZ	ADV7180 / ADV7180WBSTZ-REEL	ADV7180 / ADW10003-OREEL	ADV7180 / ADW10004-OREEL	ADV7180 / ADW10006Z-0
ADV7180 / ADW10007Z-OREEL	ADV7180 / ADW10008Z-OREEL	ADV7180 / ADW10013Z-0	ADV7180 / ADW10013Z-0RL	ADV7340 / ADV7340BSTZ

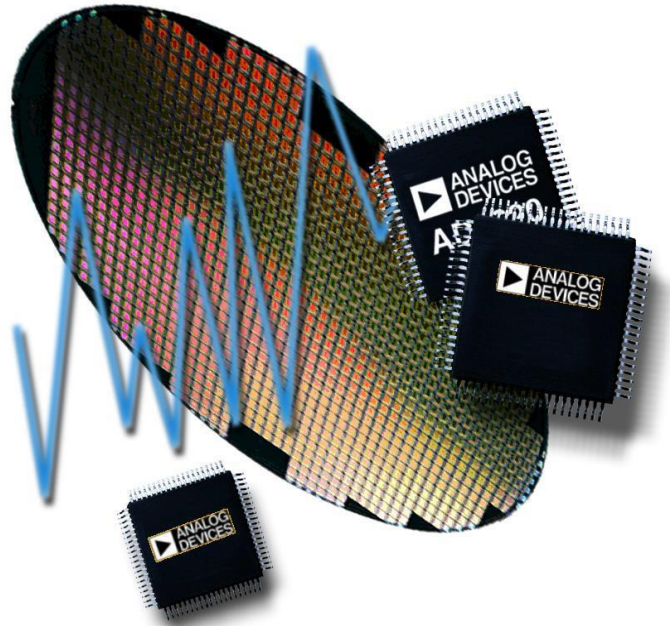
Appendix B - Revision History:

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	01-Feb-2024	05-May-2024	Initial Release.
Rev. A	01-May-2024	03-Aug-2024	Add Parts. Adding completed qualification report.
Rev. B	13-May-2024	15-Aug-2024	Correction to Delta Qualification Matrix. Add parts. Remove parts.
Rev. C	16-Dec-2024	20-Mar-2025	Remove parts. Add updated Qualification Results.



Analog Devices, Inc. PCN Material Report (Proprietary Information)

Existing Material		Material Added		Material Removed	
GENERICNUMBER	MATERIALNUMBER	GENERICNUMBER	MATERIALNUMBER	GENERICNUMBER	MATERIALNUMBER
				ADA8282	ADA8282WBPCZ
				ADA8282	ADA8282WBPCZ-R7
				ADAR7251	ADAR7251WBSCZ
				ADAR7251	ADAR7251WBSCZ-RL



Reliability Report

Report Title: Qualification of 0.18 μ m Mixed Mode CMOS Wafer Fabrication at ADI Beaverton Fab

Report Number: 21963

Revision: C

Date: 15 November 2024

Summary

This report documents the reliability qualification requirements for the release of the 0.18 μ m Mixed Mode 1.8V/3.3V CMOS Wafer Fabrication Process in Analog Devices Beaverton, Oregon (ADBN) Wafer Fabrication Facility.

The products listed below were selected to qualify the technology being released.

- The AD8283 product is a 6-channel low noise preamplifier (LNA) with a programmable gain amplifier (PGA) and anti-aliasing filter (AAF) plus one direct-to-ADC channel, all integrated with a single 14-bit analog-to-digital converter (ADC), packaged in a 72 lead LFCSP. The AD8283 is qualified to AEC-Q100 Grade 2.
- The ADV7392 product is 10-Bit SD/HD Video Encoder packaged in a 40 lead LFCSP and is qualified to AEC-Q100 Grade 2.
- The ADDR9501 product is a complete satellite digital audio radio services (SDARS) RF front-end solution, providing a complete 12.5 MHz SDARS frequency translation to a baseband signal using a single RF branch. The ADDR9501 is packaged in a 48 lead LFCSP and is qualified to AEC-Q100 Grade 2.

AECQ100 Qualification Test Methods and Summary

AEC Test Group	AEC Stress Test Name	Abbreviation	AEC Test#	Reference
Group A ACCELERATED ENVIRONMENT STRESS TESTS	Preconditioning	PC	A1	Table 2 , and Table 4
	Temperature Humidity Bias or Biased-HAST	THB or HAST	A2	
	Autoclave or Unbiased HAST or Temperature Humidity (without Bias)	AC, UHST, or TH	A3	
	Temperature Cycle	TC	A4	
	Power Temperature Cycling	PTC	A5	
	High Temperature Storage Life	HTSL	A6	
Group B ACCELERATED LIFETIME SIMULATION TESTS	High Temperature Operating Life	HTOL	B1	Table 2 , and Table 4
	Early Life Failure Rate	ELFR	B2	
	NVM Endurance, Data Retention, and Operational Life	EDR	B3	
Group C PACKAGE ASSEMBLY INTEGRITY TESTS	Wire Bond Shear	WBS	C1	<ul style="list-style-type: none"> • Test C2 (and C1 for Cu Wire) are shown in Table 4. • Tests C3-6 are qualified and controlled with inline monitors and may be viewed on-site at Analog Devices.
	Wire Bond Pull Strength	WBP	C2	
	Solderability	SD	C3	
	Physical Dimensions	PD	C4	
	Solder Ball Shear	SBS	C5	
	Lead Integrity	LI	C6	
Group D DIE FABRICATION RELIABILITY TESTS	Electromigration	EM	D1	Die Fabrication Reliability data may be viewed on-site at Analog Devices.
	Time Dependent Dielectric Breakdown	TDDDB	D2	
	Hot Carrier Injection	HCI	D3	
	Negative Bias Temperature Instability	BTI	D4	
	Stress Migration	SM	D5	
Group E ELECTRICAL VERIFICATION TESTS	Pre- and Post-Stress Electrical Test	TEST	E1	Table 5 and Table 6
	Electrostatic Discharge Human Body Model	HBM	E2	
	Electrostatic Discharge Charged Device Model	CDM	E3	
	Latch-Up	LU	E4	
	Electrical Distributions	ED	E5	<ul style="list-style-type: none"> • For Tests E5, E6 and E7, ADI New Product Yield Analysis Testing Guidelines meet AEC Q100 requirements. • Results for Tests E7-E11 are available as applicable on a case by case basis. • Test E12 results may be viewed on-site at Analog Devices
	Fault Grading	FG	E6	
	Characterization	CHAR	E7	
	Electromagnetic Compatibility	EMC	E9	
	Short Circuit Characterization	SC	E10	
	Soft Error Rate	SER	E11	
	Lead (Pb) Free	LF	E12	
	Group F DEFECT SCREENING TESTS	Process Average Test	PAT	
Statistical Bin/Yield Analysis		SBA	F2	
Group G CAVITY PACKAGE INTEGRITY TESTS	Mechanical Shock	MS	G1	< Applicable only for Cavity-Packages >
	Variable Frequency Vibration	VFV	G2	
	Constant Acceleration	CA	G3	
	Gross/Fine Leak	GFL	G4	
	Package Drop	DROP	G5	
	Lid Torque	LT	G6	
	Die Shear	DS	G7	
	Internal Water Vapor	IWV	G8	

Die/Fab Product Characteristics

Table 1: Die/Fab Product Characteristics- 0.18 μ m CMOS at ADBN

Product Characteristics	Product(s) to be Qualified		
Generic/Root Part #	AD8283	ADV7392	ADDR9501
Die Id	ND01	ND03	ND04
Die Size (mm)	7.00 x 7.00	2.57 x 3.05	3.67 x 4.54
Wafer Fabrication Site	ADBN	ADBN	ADBN
Wafer Fabrication Process	0.18 μ m CMOS	0.18 μ m CMOS	0.18 μ m CMOS
Die Substrate	Si	Si	Si
Metallization / # Layers	AlCu / 5	AlCu / 5	AlCu / 6
Polyimide	Yes	No	Yes
Passivation	SiO ₂ /SiN	SiO ₂ /SiN	SiO ₂ /SiN

Die/Fab Test Results
Table 2: Die/Fab Test Results – 0.18µm CMOS at ADBN
[Return](#)

Test Name	AEC #	Spec	Conditions	Generic/Root Part #	Lot #	Fail/SS	eTest Temp
Early Life Failure Rate (ELFR)	B2	AEC Q100-008	T _A =125°C, 48 Hours	AD8283	J90090.1	0/810	RH
					J91522.1	0/810	RH
					J91669.4	0/810	RH
			T _A =105°C, 48	ADV7392	J91084.5	1/810 ²	RH
					J90861.1	1/810 ²	RH
High Temperature Operating Life (HTOL)	B1	JESD22-A108	125°C<T _j <135°C, Biased, 1000 Hours	AD8283	J90090.1	0/77	RCH
					J91522.1	0/77	RCH
					J91669.4	1/77 ²	RCH
			125°C<T _j <135°C, Biased, 500 Hours, Ta=125°C	ADV7392	J91084.5	0/77	RCH
					J90861.1	0/77	RCH
					J90912.1	0/77	RCH
			125°C<T _j <135°C, Biased, 1000 Hours	ADDR9501	J90227.3	0/77	RCH
					J90830.1	0/77	RCH
					J90914.1	0/77	RCH
Highly Accelerated Temperature and Humidity Stress Test (HAST) ¹	A2	JESD22-A110	130C 85%RH 33.3 psia, Biased, 96 Hours	AD8283	J90090.1	1/77 ²	RH
					J91522.1	0/77	RH
					J91669.4	0/77	RH
			130°C, 85%RH 33.3 psia, biased, 96 Hours	ADV7392	J91084.5	0/77	RH
					J90861.1	0/77	RH
					J90912.1	0/77	RH
			130°C, 85%RH 33.3 psia, biased, 96 Hours	ADDR9501	J90227.3	0/77	RH
					J90830.1	0/77	RH
J90914.1	0/77	RH					

¹These samples were subjected to preconditioning at MSL 3 with 3x reflow peak temp of 260°C prior to the start of the stress test.

²These failures are attributed to silicon defects. See 8D Report 264269.

Table 3: Package/Assembly Product Characteristics

Product Characteristics	Product(s) to be Qualified		
Generic/Root Part #	AD8283	ADV7392	ADDR9501
Package	72-LFCSP	40-LFCSP	48-LFCSP
Body Size (mm)	10.00 x 10.00 x 0.85	6.00 x 6.00 x 0.75	7.00 x 7.00 x 0.85
Assembly Location	STATS (STA)	AMKOR (AP3)	AMKOR (AP1)
MSL/Peak Reflow Temperature(°C)	3 / 260°C	3 / 260°C	3 / 260°C
Mold Compound	Sumitomo G700E	Sumitomo G700	Sumitomo G700
Die Attach/Underfill/TIM	Ablestik 3230	Ablestik 3230	Ablestik 3230
Leadframe Material	Copper	Copper	Copper
Lead Finish	Sn	Sn	Sn
Wire Bond Material/Diameter (mils)	Gold / 1.00	Gold / 1.00	Gold / 1.00

Table 4: Package/Assembly Test Results

Test Name	AEC#	Spec	Conditions	Generic/Root Part #	Lot #	Fail/SS	eTest Temp
Preconditioning	A1	J-STD-020	MSL-3	ADV7392	J91084.5	0/15	R
					J90861.1	0/15	R
					J90912.1	0/15	R
				AD8283	J90090.1	0/15	R
					J91522.1	0/15	R
					J91669.4	0/15	R
				ADDR9501	J90227.3	0/15	R
					J90830.1	0/15	R
					J90914.1	0/15	R
High Temperature Storage Life (HTSL)	A6	JESD22-A103	+150°C, 500 Hours	ADV7392	J91084.5	0/45	RH
				AD8283	J90090.1	0/45	RH
			+150°C, 1000 Hours	ADDR9501	J90227.3	0/45	RH
Temperature Cycling (TC) ¹	A4	JESD22-A104	-55°C/+125°C, 1000 Cycles	ADV7392	J91084.5	0/77	RH
					J90861.1	0/77	RH
					J90912.1	0/77	RH
				AD8283	J90090.1	0/77	RH
					J91522.1	0/77	RH
					J91669.4	0/77	RH
				ADDR9501	J90227.3	0/77	RH
					J90830.1	0/77	RH
					J90914.1	0/77	RH
Unbiased HAST (UHST) ¹	A3	JESD22-A118	+130°C, 85%RH 33.3 psia, 96 Hours	ADV7392	J91084.5	0/77	R
					J90861.1	0/77	R
					J90912.1	1/77 ²	R
				AD8283	J90090.1	0/77	R
					J91522.1	0/77	R
					J91669.4	0/77	R
				ADDR9501	J90227.3	0/77	R
					J90830.1	0/77	R
					J90914.1	0/77	R
Post-TCT WBP	C2	MIL-STD883 Method 2011	3gF	ADV7392	J91084.5	0/5	N/A
				AD8283	J90090.1	0/5	N/A
				ADDR9501	J90227.3	0/5	N/A

¹ These samples were subjected to preconditioning at MSL 3 with 3x reflow peak temp of 260°C prior to the start of the stress test.

² These failures are attributed to silicon defects. See 8D Report 264269

ESD and Latch-Up Test Results

Table 5: ESD Test Results
[Return](#)

ESD Model	Generic/Root Part #	Package	ESD Test Spec	RC Network	Highest Pass Level	Class	eTest
FICDM	ADV7392	40-LFCSP	JS-002	1Ω, Cpkg	±500V	C2	RH
	AD8283	72-LFCSP			±450V ¹	C1	RH
	ADDR9501	48-LFCSP			±750V	C2	RH
HBM	ADV7392	40-LFCSP	JS-001	1.5kΩ, 100pF	±2000V	1C	RH
	AD8283	72-LFCSP			±2000V	1C	RH
	ADDR9501	48-LFCSP			±2000V	1C	RH

1. Control Material Passes Same Threshold

Table 6: Latch Up Test Results
[Return](#)

LU Test Spec	Generic/Root Part #	Passing Current	Passing Over-Voltage	Temperature (T _A)	Class	eTest
JESD78	ADV7392	+100mA, -100mA	+2.84V/+5.2V/+5.45V	+105°C	II	RH
	AD8283	+100mA, -100mA	+2.85V, +5.1V	125°C	II	RH
	ADDR9501	+100mA, -100mA	+2.85V, +5.1V	+85°C	II	RH

Approvals

Reliability Engineer: Denis Belisle

DeltaQualificationMatrix

General

Short product and technology cycles as well as new environmental regulations frequently result in process and material changes of components, printed circuit boards, assembly techniques and circuit layout which have to be evaluated. The ZVEI "Guideline for Customer Notifications of Product and /or Process Changes (PCN) of Electronic Components specified for Automotive Applications" describes an appropriate methodology for dealing with changed electronic components. The qualification matrices in this guideline are recommendations for how to assess typical changes of electronic components. These recommendations promote an open risk-based discussion between supplier and customer regarding qualifications.

The DeltaQualificationMatrices were developed by the Industry Task Force Team "PCN DeltaQualificationMatrix" together with component experts from the ZVEI Working Group "PCN-Methodology". Actual content represents state-of-the-art technology and does not claim to be comprehensive. Deviation from proposed guideline should be mutually agreed as customer specific requirements have to be considered.

DeltaQualificationMatrix Application (completion by component manufacturer)

- a) This table has to be used for changes only. The matrices are not applicable for new product, special qualifications (for instance for encapsulation of module) or Information Notes.
- b) If a change is not listed in this table, the qualification plan has to be defined and agreed between customer and supplier.
- c) The matrix for Active Components requires the user to choose between integrated circuits AEC-Q100 and discrete semiconductors AEC-Q101 (cell D4).
For Passive Components AEC-Q200 is used. For Optoelectronic Components the AEC-Q102 is used.
For Multi-Chip-Modules the AEC-Q104 is used. For MEMS the AEC-Q103 is used.
- d) All changes as listed in the PCN have to be marked by a cross (x) in column B and will appear colored. The relevant reliability tests are then shown in "Tests, which should be considered for the appropriate process change".
- e) In "Tests, which should be considered for the appropriate process change after selection of condition table" is for modification of the found relevant tests under consideration of the weight of change.
Related table "Conditions" has to be assessed per proposed letters with an (x).
- f) In "Suppliers performed tests" the component manufacturer documents the planned and performed tests.
- g) In case of deviations from tests, which should be considered this should be notified and commented by the component manufacturer in the area "Reason for exception of tests".
Test results in form of generic data (G) are allowed when notified and justified.

Evaluation Levels are categorized as follows

"C: Component level": The evaluation of a change at component has to be done by the component manufacturer at the component only. Generic data from other relevant evaluations can be used.

"B: Board level": The intended change described in the PCN may influence handling/processability/manufacturability of the component at the customer. Therefore, additional evaluation by the customer may be necessary.

"A: Application level": The intended change described in the PCN may influence the properties of the application (e.g. ECU). In addition to the evaluation under C or B the influence of the change in the application is evaluated by suitable investigations by the customer. It has to be considered whether the application / assembly requirements are already sufficiently safeguarded by other qualifications (application-specific risk assessment).

"* : Not relevant for qualification matrix": Changes which fulfill neither A,B nor C definitions

Information Notes

Changes indicated as "I" shall not be marked in the DeQuMa. For those changes the Information Note sheet shall be used. As the DeQuMa is desired for PCN only, a marking of "I"-changes would automatically influence evaluation level and test effort.

Important Notes

- To use the matrices in the right form the ZVEI working group provides a Tutorial on its homepage (ZVEI-Tutorial)
- ID number: is a unique identification number for each indicated change defined in the ZVEI PCN DeltaQualificationMatrices. The same ID number is used in the PCN Form sheet to identify the change.
- Tests identified by the matrix have to be considered and checked if they are necessary to assess the specific change. Test modifications or generic data have to be justified in detail.
- "Further applicable conditions", comments and notes need attention, as they provide important hints and limitations.
- In order to use all functions in EXCEL, macros have to be allowed.

Form provided by ZVEI - Revision 5.0 - December 2021

History of DeQuMa

Version	Remarks
2.0	Revised by ZVEI PCN Methodology Workgroup in March 2015
2.1	Released March 2015
2.1.1	Active Components - delete write protection in comments
2.2	Solved problems with some ActiveX configurations
2.2.2	Solved Problems in Active Components
2.2.3	Solved Problems ActiveX, Active Components SEM-DE-02 (Design changes in routing) error fixed
2.2.4	Minor fixes
3.0	General Revision by ZVEI PCN Methodology Workgroup in June 2016 Changes are indicated by underlining in the read only version named Changes_DeQuMa_rev3_vs_rev2.xlsx
3.0.4	Expert Release
3.0.5	Fixing of macro bugs
3.1	Final Release (orthographic and punctuation corrections)
4.0	General Revision by ZVEI PCN Methodology Workgroup in July 2019. Muliti Chip Modules newly added to DeQuMa LED Components now based on the AEC Q102 Further Changes see separate PDF's Excel-File , where changes are indicated by underlining
4.1	LED worksheet: Content of columns had been swapped due to rearrangement and omission of columns.
5.0	General Revision by ZVEI PCN Methodology Workgroup in October 2021. Add MEMS pressure sensor

